



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

DOTTA et al

Atty. Ref.: 1035-472

Serial No. 10/670,244

Group: 2811

Filed: September 26, 2003

Examiner:

For: SEMICONDUCTOR DEVICE AND CHIP-STACK
SEMICONDUCTOR DEVICE

* * * * *

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

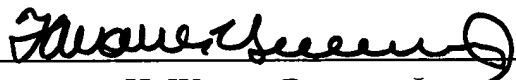
REQUEST FOR CORRECTION OF FILING RECEIPT

Enclosed is a copy of the Filing Receipt for the above-identified application which incorrectly states the second inventor's name as "Toshi Kimura". It should read "Toshio Kimura". The correction is shown in red on the attached copy of the filing receipt. Please note your records accordingly. Correction of same is respectfully requested.

Respectfully submitted,

NIXON & VANDERHYE P.C.

January 29, 2004

By: 

H. Warren Burnam, Jr.

Reg. No. 29,366

HWB:lsh
1100 North Glebe Road, 8th Floor
Arlington, VA 22201-4714
Telephone: (703) 816-4000
Facsimile: (703) 816-4100

1035-472



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/670,244	09/26/2003	2811	1062	1035-472	13	28	5

23117
NIXON & VANDERHYE, PC
1100 N GLEBE ROAD
8TH FLOOR
ARLINGTON, VA 22201-4714



CONFIRMATION NO. 8593

FILING RECEIPT



OC000000011544503

Date Mailed: 12/22/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Yoshihisa Dotta, Nara-shi, JAPAN;
~~Toshi Kimura~~, Kitakatsuragi-gun, JAPAN;
Toshio Kimura

Assignment For Published Patent Application

SHARP KABUSHIKI KAISHA, Osaka, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-313526 10/28/2002

If Required, Foreign Filing License Granted: 12/19/2003

Projected Publication Date: 04/29/2004

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor device and chip-stack semiconductor device